

FBIS-2457/88 29 February 1988

MEMORANDUM FOR:	S1/OIT	STAT
FROM:	AFS Classified Segment Manager Advanced Development Division, ESG/FBIS	STAT
SUBJECT:	AFS/OIT Interface Control Document	

- 1. Per our previous phone conversation, I am replying to concerns over the TBD's and the lack of a verification matrix in the ICD we have submitted for approval.
- 2. The document contains one TBR on page 5 in paragraph 1.4.2.1 16,000,000 (TBR). This number is now firm at 5,000,000 and the TBR can be removed. The one TBD on page 91 under paragraph H.1 Hardware is a 'don't care' to FBIS. Whatever OIT decides is what it is.
- 3. Attached is a copy of the verification matrix we have in the AFS Functional Specification. We plan to provide a similar matrix for the ICD. However, as I indicated in a previous memo, we have directed our contractor to stop all work on the interface until the ICD is approved. We estimate that the ICD Verification Matrix will be completed approximately two months from turn-on of work.

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Attachment: As Stated

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SUBJECT: AFS/OIT Interface Control Docment

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## 4.0 QUALITY ASSURANCE

Verification that the segment's performance satisfies the requirements defined in paragraph 3.0 shall be accomplished as indicated by Table 4-1. Entries for this table are explained in the following paragraphs.

# 4.1 Verification Method (1)

The verification method establishes how a given system shall be verified. The verification methods shall be:

#### **ENTRY**

- NA Not Applicable This designation indicates that the applicable entry does not require verification. This is usually used on requirements that are fully allocated with apriori review and approval.
- IN Inspection An observation or examination of an item against the applicable documentation to verify compliance with the requirements.
- AN Analysis Inspection with interpretation, interpolation or extrapolation of processes or of analytical/empirical data under defined conditions or reasoning to show theoretical compliance with the stated requirements. It also includes the use of a computer model with the appropriate statistical processes for simulating the functional and/or operational characteristics of an item to verify the stated requirements.
- DE Demonstration Verification of an operational or functional capability by one or more performances before qualified vitnesses. Instrumentation or data recording beyond that provided indigenously by the elements being verified shall generally not be required.
- TE Test Verification of a performance requirement under specific conditions involving the use of instrumentation, special test software, and/or special test equipment to generate, acquire, and record data. This method includes the analysis of test data. Format test procedures are conducted before qualified witnesses.

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4.2 Verification Level  $(\omega/2)$ 

#### ENTRY

- MU Multisegment Verification that the interfaces between two or more segments are compatible and together provide the required system performance. Multisegment verification requires elements from two or more segments or validated simulations operating in conjunction.
- SE Segment Verification is required at the segment level.
- SU Subsystem Verification is required at the subsystem level.
- 4.3 Verification Category (41.3)

The verification category establishes when and by whom the verification shall be performed. One or more of the following verification categories may apply. The presence of two categories in a Table 4-1 entry means that the requirement must be verified at each category.

## **ENTRY**

- SR System Design Review Very early verification that the applicable requirements are met by system architecture, conceptualization, etc. Verification will be accomplished by the design review committee.
- MR Module Design Review Early verification that the selected design will adhere to the required specifications.
- VV Vendor Verification The vendor performs a verification process (either that specified or an equivalent). The process shall be approved by LEC prior to performance. The verification procedures shall be designed and written such that the test results are repeatable.
- LV Lockheed Verification LEC performs the specified verification process in an acceptance test procedure. The verification procedures shall be designed and written such that the test results are repeatable.

## 4.4 Verification Location (o/.4)

This entry specifies where the test shall be performed. One or more of the following verification locations may apply. The presence of two levels in a Table 4-1 entry means that the requirement must be verified at each location.

## **ENTRY**

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- VP Vendor's Plant The verification process shall take place at the vendors location.
- LP Lockheed Plant The verification process shall take place at the Lockheed Plant in addition to tests at any other location.
- CI Customer's Installation The verification process shall take place at the final location in addition to tests at any other location.