2 of 5

[redacted] STAT

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Original and 1 to [redacted] of TID on 25 Jan 1966.

24 January 1966

CA-18557

Headquarters, Logistical Support Group (Provisional)
Headquarters Command
United States Air Force
Bolling Air Force Base, D. C. 20332

Attention: [redacted] STAT

Subject: Quotation for AP-3 Computer Programming Course

Gentlemen:

In accordance with your discussions with our [redacted] The [redacted] offers to provide a training course in AP-3 computer programming. The course would be three weeks in duration and would be provided for a class of from one to four persons. The class would be conducted at the [redacted] facility in [redacted] Suggested dates are 11 April 1966 through 29 April 1966. The course is offered for a fixed price of [redacted] STAT

An Analysis of Estimated Cost is attached which provides a cost breakdown. Terms are net 30 days. This quotation will remain valid for a period of sixty (60) days from the date of this letter.

An outline showing the general scope of the course is attached. The course is intended for personnel with some computer programming experience, but little or no knowledge of the AP-3 system. In addition to classroom sessions, training sessions would be provided on the AP-3 computer itself, with personnel writing and checking .

STAT

Hqtrs., Logistical Support Group (Provisional)

24 January 1966
CA-18557

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We look forward to hearing from you in regard to this proposal. In any negotiations, the [redacted] would be represented by the undersigned. The telephone contact is: area code [redacted] Administrator, on Extension 245. If additional information is desired, please do not hesitate to contact us.

STAT

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Very truly yours,

[redacted]

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General Manager and Director

Attachments: Analysis of Estimated Cost
AP-3 Computer Programming, Course Outline

ECJ/ea

Attachment
CA-18557

AP-3 Computer Programming

COURSE OUTLINE

1. General computer organization, functions in the AP-3 stereoplotter system, standard programs and their functions.

2. Internal computer timing, memory organization, instruction and data-word formats, instruction register, instruction decoding

3. Accumulator and arithmetic registers, control logic, tape input-output logic, operating controls and displays

4. Instruction execution: read-write, add-subtract, shift, multiply-divide, control instructions, tests and transfers

5. Flow charts, coding sheets, coding concepts, subroutines, program-preparation routines

6. Organization of incremental computation section, integrator description, interconnections of integrators, data formats

7. Programming incremental section, scaling, timing, instruction formats and codes, rate-limiting, function generator, handwheel input and servo output

8. Programming demonstrations; definition, flow-charting, and coding of sample problems; tape preparation and program testing

9. Student problems

10. Review