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NPIC/P&DS/D/6-730  
20 January 1966

MEMORANDUM FOR: Chief, Support Systems Section, DB/P&DS

25X1A

ATTENTION :

THROUGH : Chief, Equipment Performance Section, DB/P&DS

*RBN*

SUBJECT : Chip Comparator Reliability

1. The reliability of the  digitizing components in the chip comparator system installed in IAD is extremely poor. The circuitry must be constantly adjusted to compensate for variations in the interferometer signals, and, when this is no longer possible due to range limitations, the interferometer outputs must be adjusted. These adjustments have been required at least once a week and are difficult and time consuming.

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2. Because the prototype system experienced the same failure rate, it must be assumed that the other systems about to become operational will, also. Six or more comparators needing a lengthy tune-up weekly is an unconscionable situation.

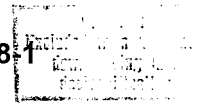
3. The following is a list of dates when one or both axis failed on IAD's instrument. It should be noted that this reflects a light work load and heavier use would probably require more frequent adjustments.

<u>Axis Counting Incorrectly</u>	<u>Date</u>
X	19 Nov 65
X - Y	9 Dec 65
X - Y	30 Dec 65
X - Y	3 Jan 66
X	12 Jan 66
X (Impossible to correct-all controls at limit of range)	19 Jan 66

DECLASS REVIEW by NIMA/DOD

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4. The manufacturer should be contacted regarding this problem, as a "fix" is clearly necessary.



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Equipment Performance Section

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cc:



Distribution:

Orig & 1 Addressee

2 P&DS/DB

ILLEGIB



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