

Declassified in Part - Sanitized Copy Approved for Release 2013/12/04 : STAT
CIA-RDP78-03535A002000010010-6

PROJECT 2529

**SEQUENTIAL PULSE ADDRESSING
CIRCUITRY**

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DESIGN

Circuit Development
2 months

Prototype
Fabrication
1 month

Tests
Modif
2 wks

Fabrication of
10 units
2 months

Tests
Modif
2 wks

3 Engineers

FABRICATION

Parts for 10 production
units by now on order.

Order all unforseen parts

Problems

Problems,
tests,
assistance

2 Technicians Direct production super-
vision and quality control

One girl

2 girls
Memory & Address

One girl

2 girls
Electronics

One girl

2 girls
Power & Chassis

DRAFTING

Module Boards 1 man

Instructions

Manuals

SHOP

Mech. support

One Engr.
One Mach't
constr'n

One Engineer
One Machinist
All mech construction

prepare
boards for
10 units

STATION "A"

Module boards
prototype

Module boards
for 10 units

Instruction
manuals

SECRETARY

PROJECT 2529/

Tentative Development and Production Schedule

STAT

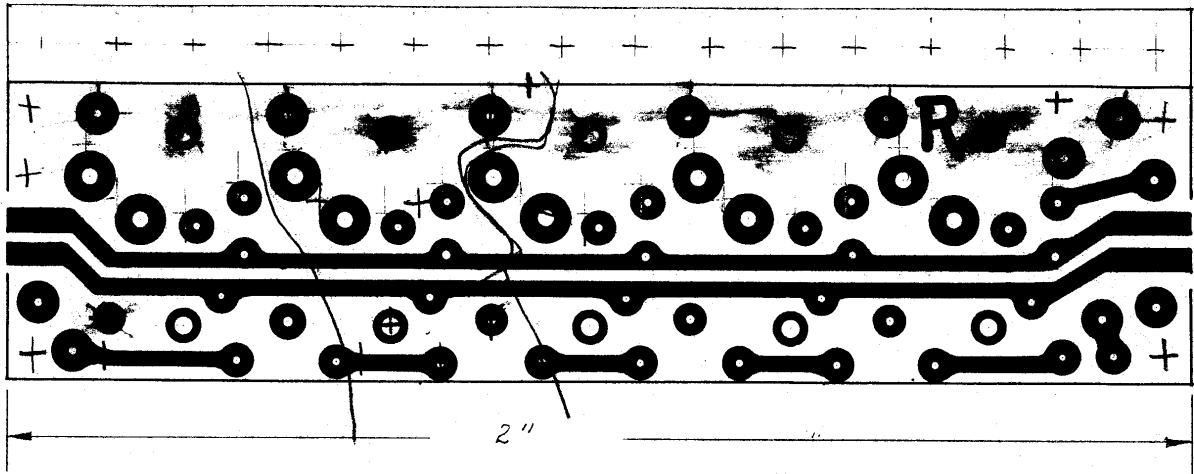
See Reverse for Review Action

Sequential Pulser

5 Aug 60 In next model after prototype;

1. increase winding turns in col. cir. on "D" cores to assist bit transfer (not final)
2. reduce resistive load on cl-2 wdg. of "D" cores to stretch conduction interval - 4 μ sec
3. increase feedback turns accordingly.

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Make 4 Bonds Right side up
 " 2 " Wrong side up or Reversed (R)

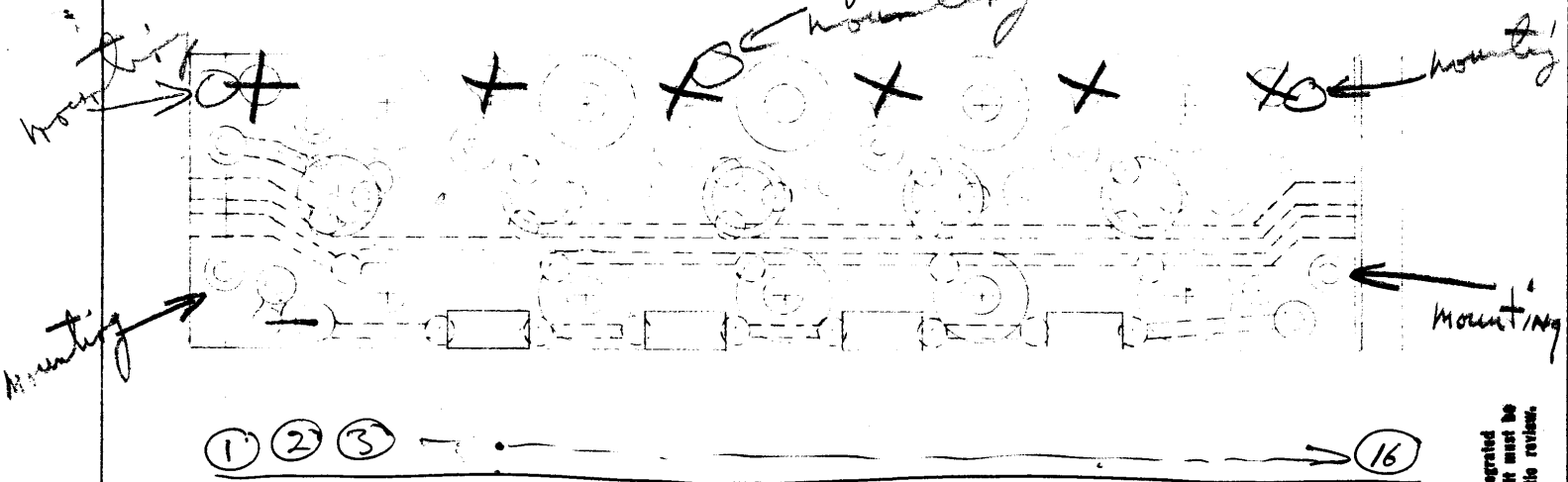
OBSOLETE
 JVC

SCALE: 1" = 1/4"	
DRAWN BY:	DATE: 2-5-60
CHKD. BY:	DWG. NO.:
APPD. BY:	R529

- ① GND
- ② Adv (-)
- ③ clear (-)
- ④ a'
- ⑤ b'

- ⑥ out 1
- ⑦ out 2
- ⑧ out 3
- ⑨ out 4
- ⑩ out 5

- ⑪ clear (+)
- ⑫ Adv (+)
- ⑬ a
- ⑭ b
- ⑮ input (dot) (-)
- ⑯ input

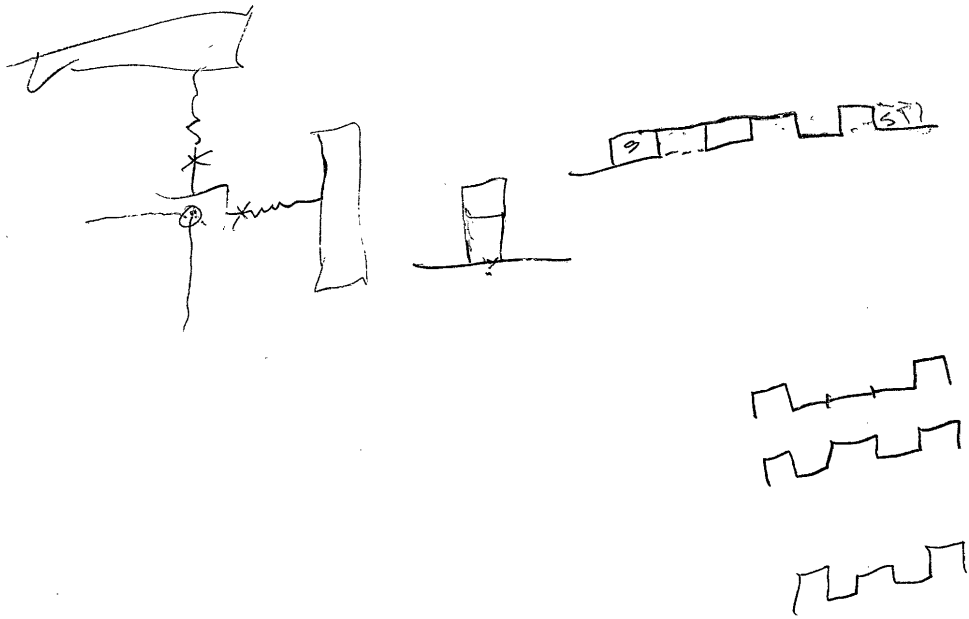


TOP BOARD ONLY
 * (other boards have only 14 term.)

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Shift Register

DRAWN BY:	DATE:
CKD. BY:	DWG. NO.:
APPD. BY:	



NOTICE

Before building any more S.P.'s, check with G.H.S. concerning reduction of "D" core load resistance, so as to lengthen switching time + improve S.P. driver tolerances. Also: R.C. resistors.

22 Dec. '60

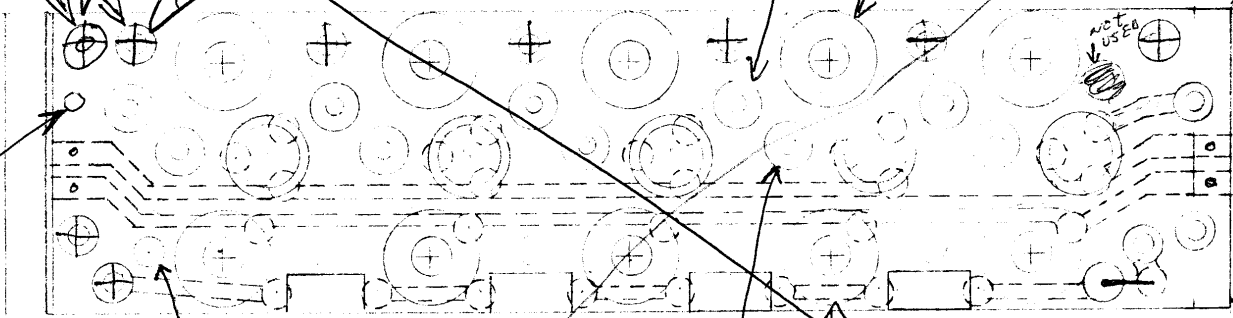
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clear & adv. term.
wires from
bottom board
are connected
here

clear
adv

TOP
TERMINAL
TOP
TORROID

TOP



BOTTOM

NOT USED

Bottom
TORROID

Bottom
TORROID

stages numbered
from right to left
(1 thru 5)

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ADVANCE
AND CLEAR windings
fed through here to
the top board
Also "A" & "B" pass
through here from the
top board to the
bottom board

Middle Board

1st stage showed
high (500 or 1K or 5000)
resistance B & C

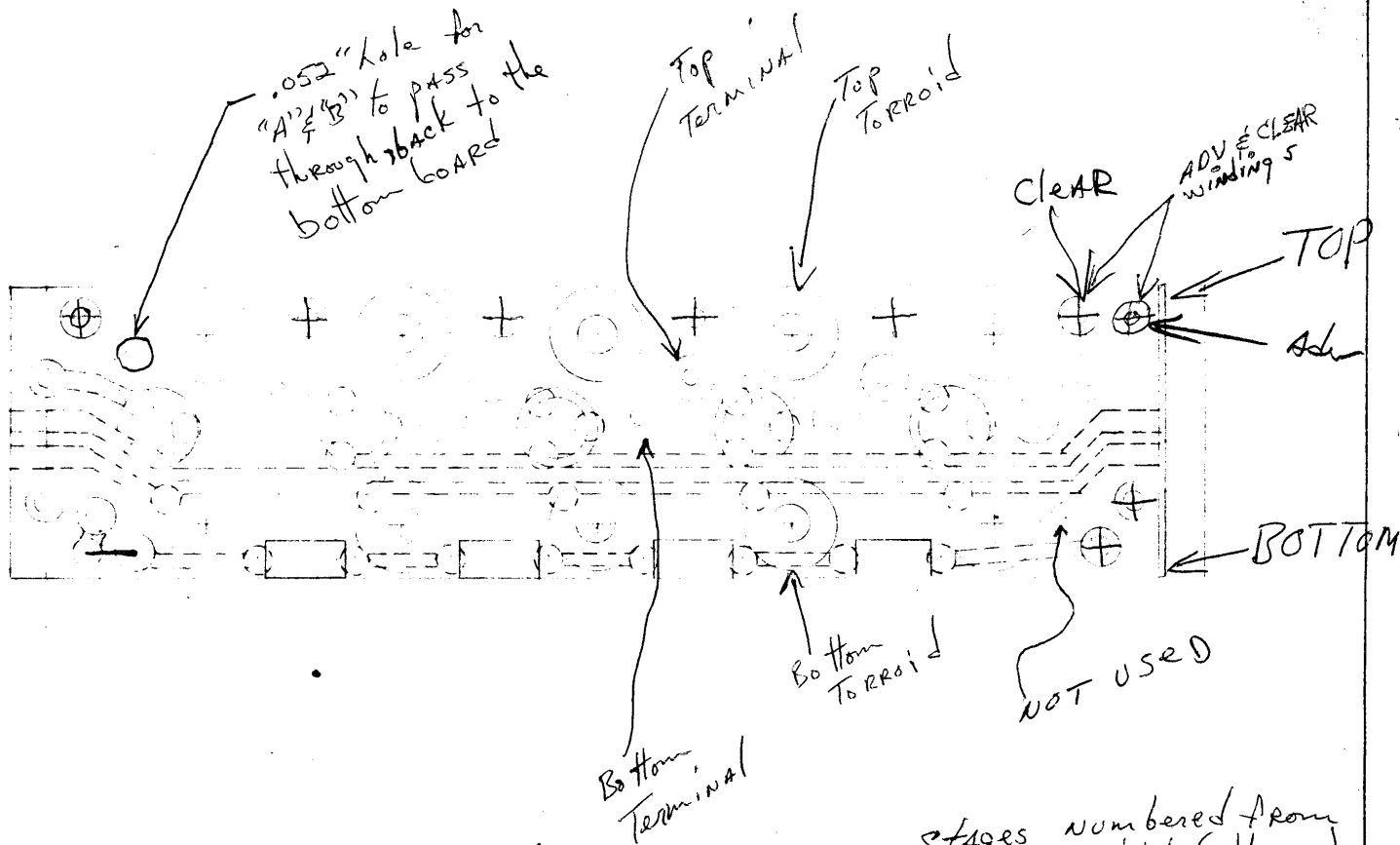
DATE: 6-1-55	DRAWN BY: YB
DWG. NO.: 5003	COP. BY: YB
	APP. BY: YB

Middle Board

- 1) leave base lead eyelet U.S.
- 2) leave top eyelet of first stage U.S.
- 3) leave a #6 (of last stage) long

This board is now the same as the bottom board, but it is positioned so that the stages (6-10) run from right to left. And the outputs are on the same side as the outputs on the top & bottom boards!

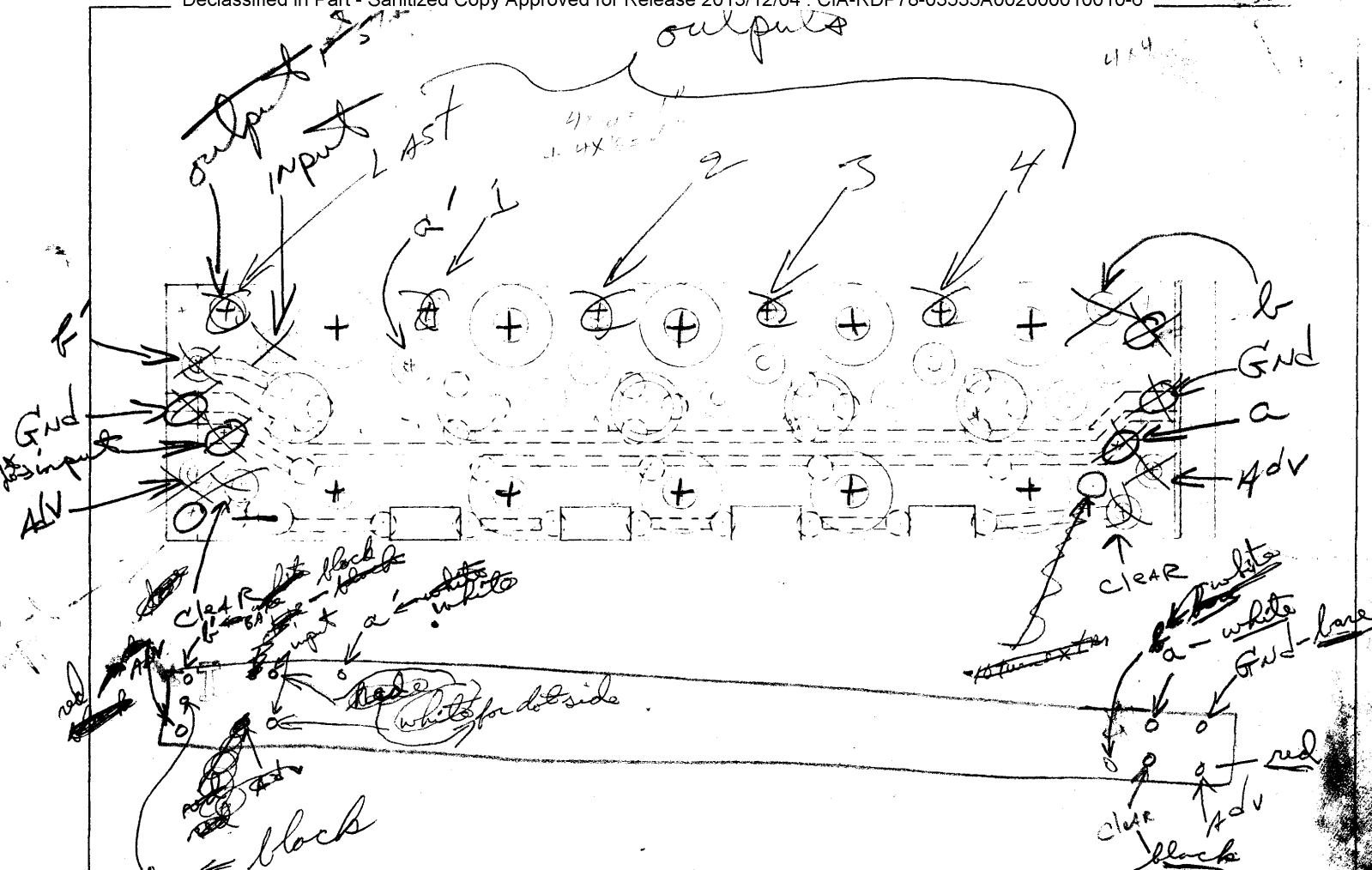
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Top Board

DRAWN BY:	DATE: 2-11-65
CHKD. BY:	DWG. NO.:
APPD. BY:	



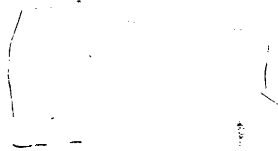
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Shift Register

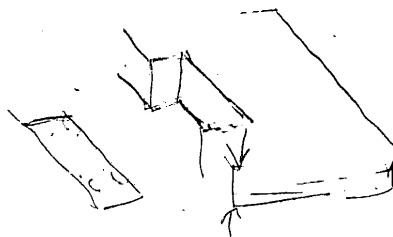
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APPD. BY:	

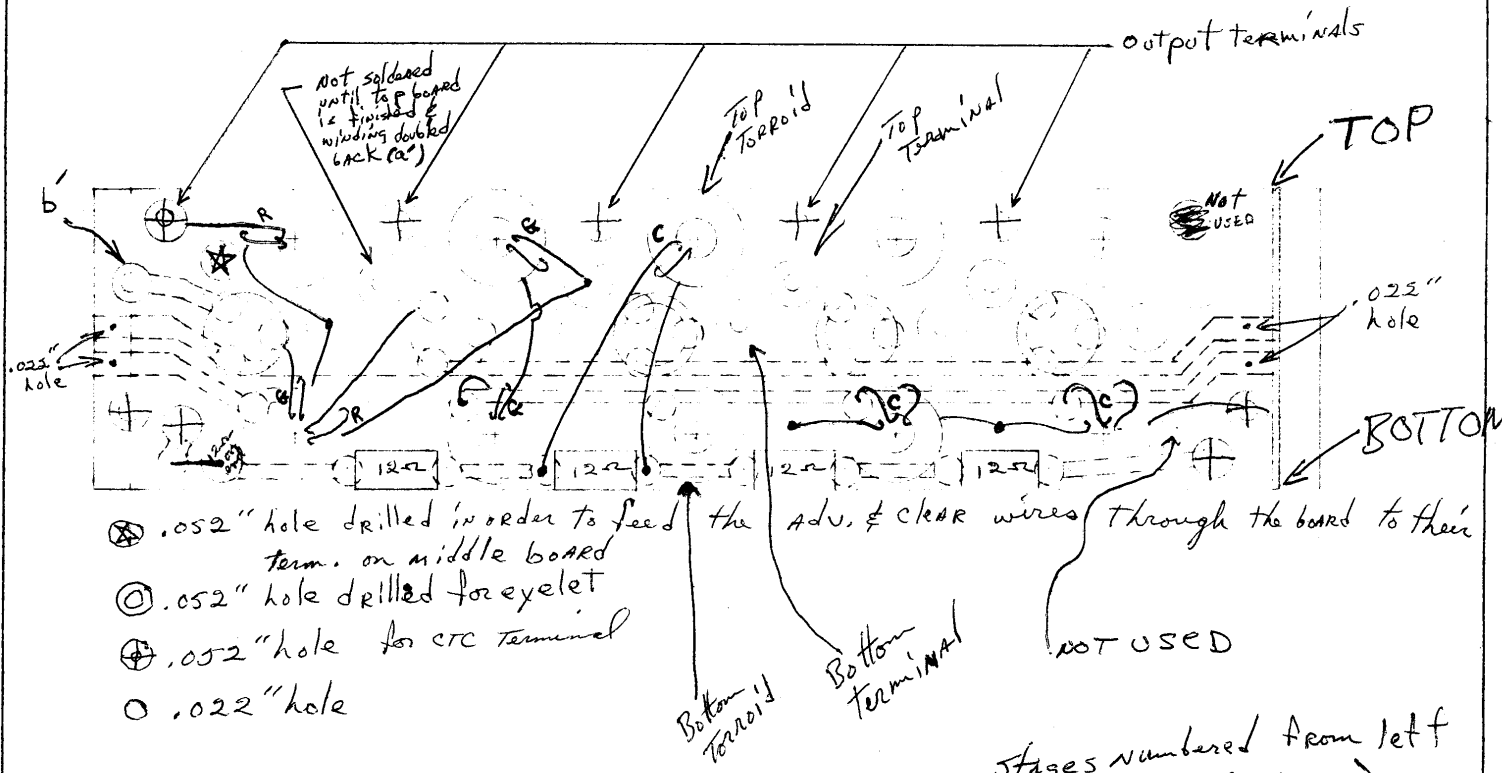
C-B

F
R



$$\frac{75.052}{\sqrt{15.0}}$$





- ⊗ .052" hole drilled in order to feed the ADV. & CLEAR wires through the board to their term. on middle board
- ⊙ .052" hole drilled for eyelet
- ⊕ .052" hole for CRC terminal
- .022" hole

R - Red
 C - CLEAR
 G - Green
 } colours of windings

- stages numbered from left to right (1 thru 5)

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Bottom board (5P)

DRAWN BY:	DATE: 2-4-60
CKD. BY:	DWG. NO.:
APPD. BY:	

Bottom Board

- 1) leave eyelet in base lead of 1st stage N.S.
- 2) leave Top eyelet of 1st stage N.S.
- 3) leave leads A & B of 5th stage long & N.S. to anything
- 4) ORDER of PARTS placement:
 - A. Top TORROID
 - B. Bottom TORROID
 - C. RESISTORS AND TRANSISTORS
- 5) PLACE top TORROID over mounting post AND place all leads in their proper position
- 6) Take bottom TORROID & hold it just over top of its mounting post put on the two turn green winding with the long green lead from the top TORROID, then place all leads in their proper positions

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~~Always put~~